

CSE 260M - Quiz 6

October 11, 2006

Name: _____

Student ID Number: _____

1. Design a minimal, two-level, SOP logic circuit that detects when the 4-bit input, $A_3A_2A_1A_0$, when considered to be an unsigned number, is odd. For odd inputs, the output should be '1', and for even inputs the output should be '0'. Use a K-Map, and draw the logic circuit.

You can assume that the inputs in uncomplemented and complemented form are available.